Quiz 3: Arithmetic Shift Instructions

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Question 1 (3 points)   ✓ *Saved*

Assume that the content of R2 is 0x28E0D190; Select the flag(s) that is/are set when the following instruction is executed: CMP   R2, R2, ASR #2

○ a) N Flag

○ b) Z Flag

○ c) V Flag

○ d) C Flag

○ e) None

Question 2 (3 points)

Assume that the content of R7 is 0x200A9F0E; Select the flag(s) that is/are set when the following instruction is executed: TST   R7, R7, ASR #3

○ a) N Flag

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1 ✓	2 --	3 --
4 --	5 --	

Question 2 (3 points)

Assume that the content of R7 is 0x200A9F0E; Select the flag(s) that is/are set when the following instruction is executed: TST   R7, R7, ASR #3

- ◯ a) N Flag
- ◯ b) Z Flag
- ◯ c) V Flag
- ◯ d) C Flag
- ◯ e) None

Question 3 (3 points)

Assume that the content of R4 is 0x0902205D; Write the content of the indicated Register when the following instruction is executed: SUB   R2, R4, R4, ASR #3

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1 ✓	2 --	3 --

4 --	5 --

Question 4 (3 points)

Assume that the content of R14 is 0xAAE0DE90; Select the flag(s) that is/are set when the following instruction is executed: CMN   R14, R14, ASR #1

- [ ] a) N Flag
- [ ] b) Z Flag
- [ ] c) V Flag
- [ ] d) C Flag
- [ ] e) None

Question 5 (3 points)

Assume that the content of R2 is 0x002111AB and C flag is clear; Write the content of the indicated Register when the following instruction is executed: SBC   R4, R2, R2, ASR #4